Figure 1 Computer Display System
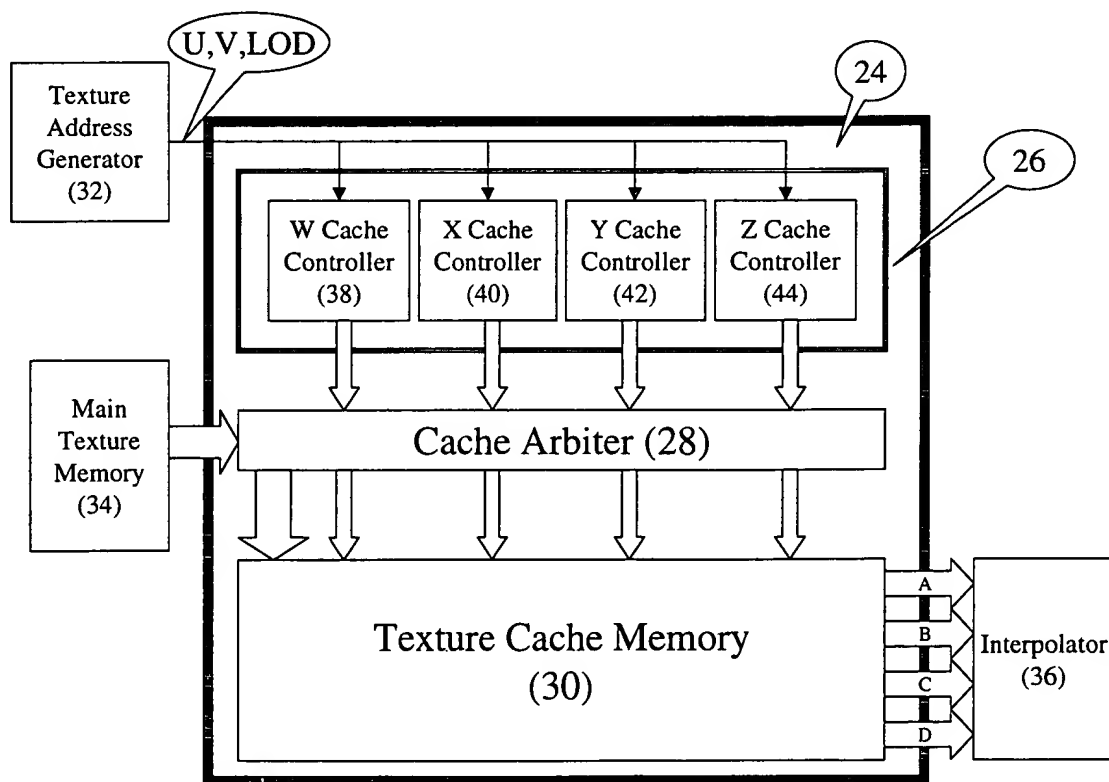

Figure 2 Texture Cache System
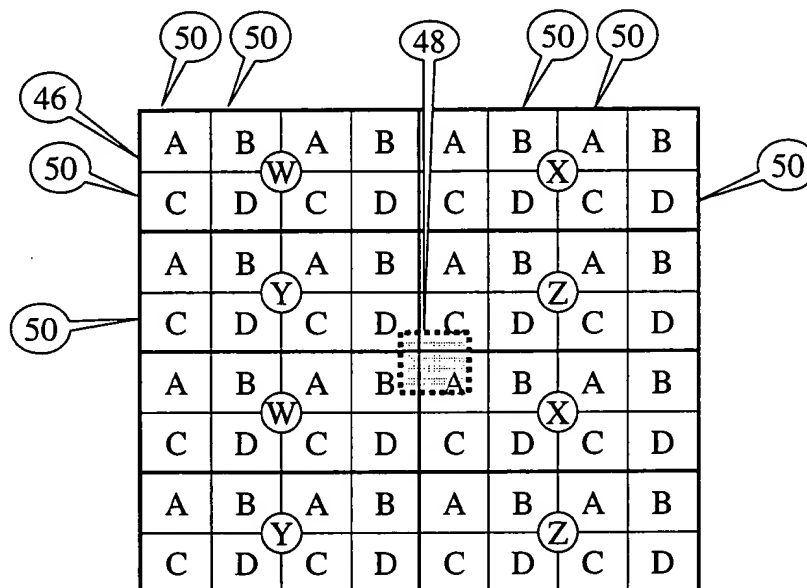
50 50 48 50 50

46

50

50

50

A	B	A	B	A	B	A	B
C	D	C	D	C	D	C	D
A	B	A	B	A	B	A	B
C	D	C	D	C	D	C	D
A	B	A	B	A	B	A	B
C	D	C	D	C	D	C	D
A	B	A	B	A	B	A	B
C	D	C	D	C	D	C	D

W X

Y Z

W X

Y Z

Figure 3 Texel Type

52 54 52

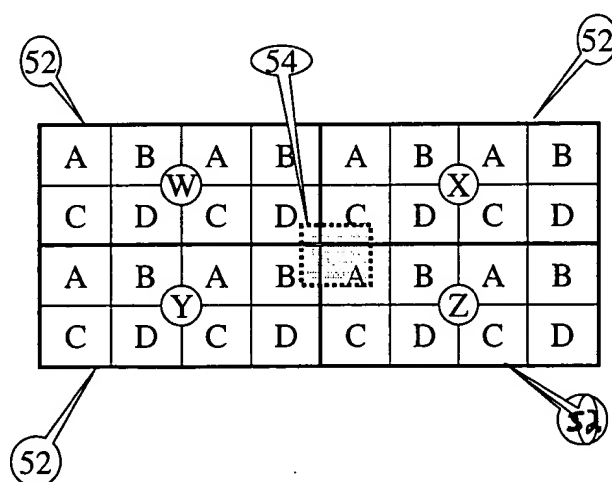| A	B	A	B	A	B	A	B
C	D	C	D	C	D	C	D
A	B	A	B	A	B	A	B
C	D	C	D	C	D	C	D

W X
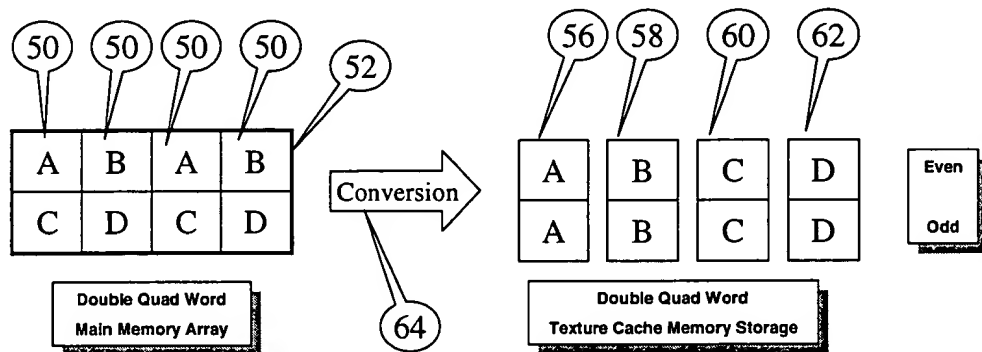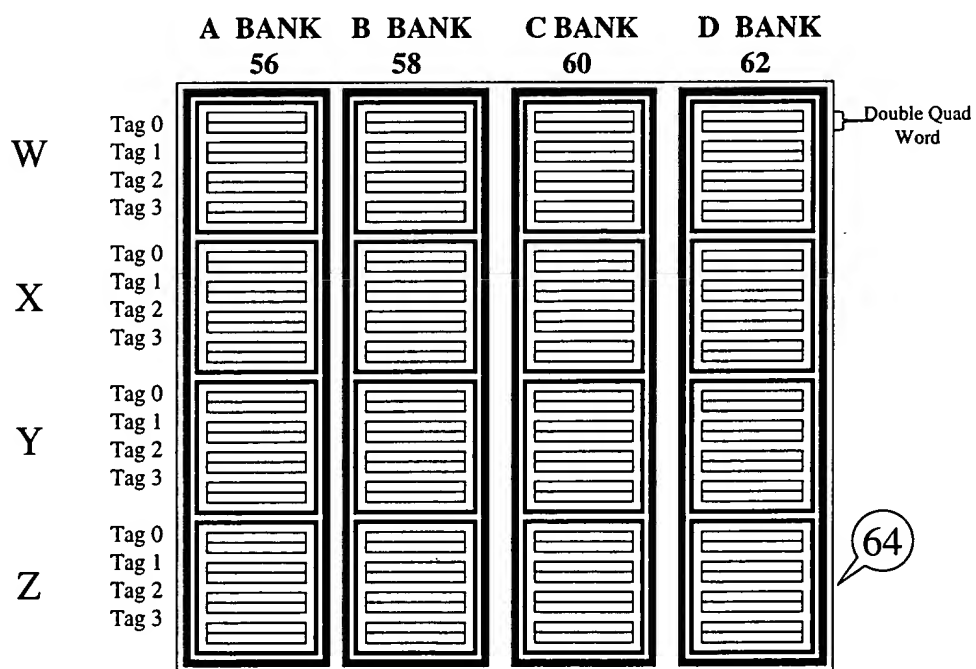
Y Z

52

52

52

Figure 4 Texture Block Identifiers

Figure 5 Memory - Cache Mapping

Figure 6 Texture Cache Memory

Figure 7 Texture Cache Controller

Figure 8 Texture Cache Arbiter